

DUAL EDGE PROGRAMMABLE DELAY UNIT

ABSTRACT OF THE DISCLOSURE

A method and device program a dual edge programmable delay unit, that responds to an input signal with a rise time and a fall time, includes a buffer which
5 receives the input signal and provides an output signal with programmed variable delays between the rise and fall times of the output signal. Programmable control sources (PCS) provide separate control inputs to a buffer. The FTPCS charges a capacitor in the buffer when the input signal changes from high to low to adjust time delay before the fall of the buffer output signal. The RTPCS discharges the
10 capacitor in the buffer when the input signal changes from low to high to adjust time delay before the rise of the buffer output signal.